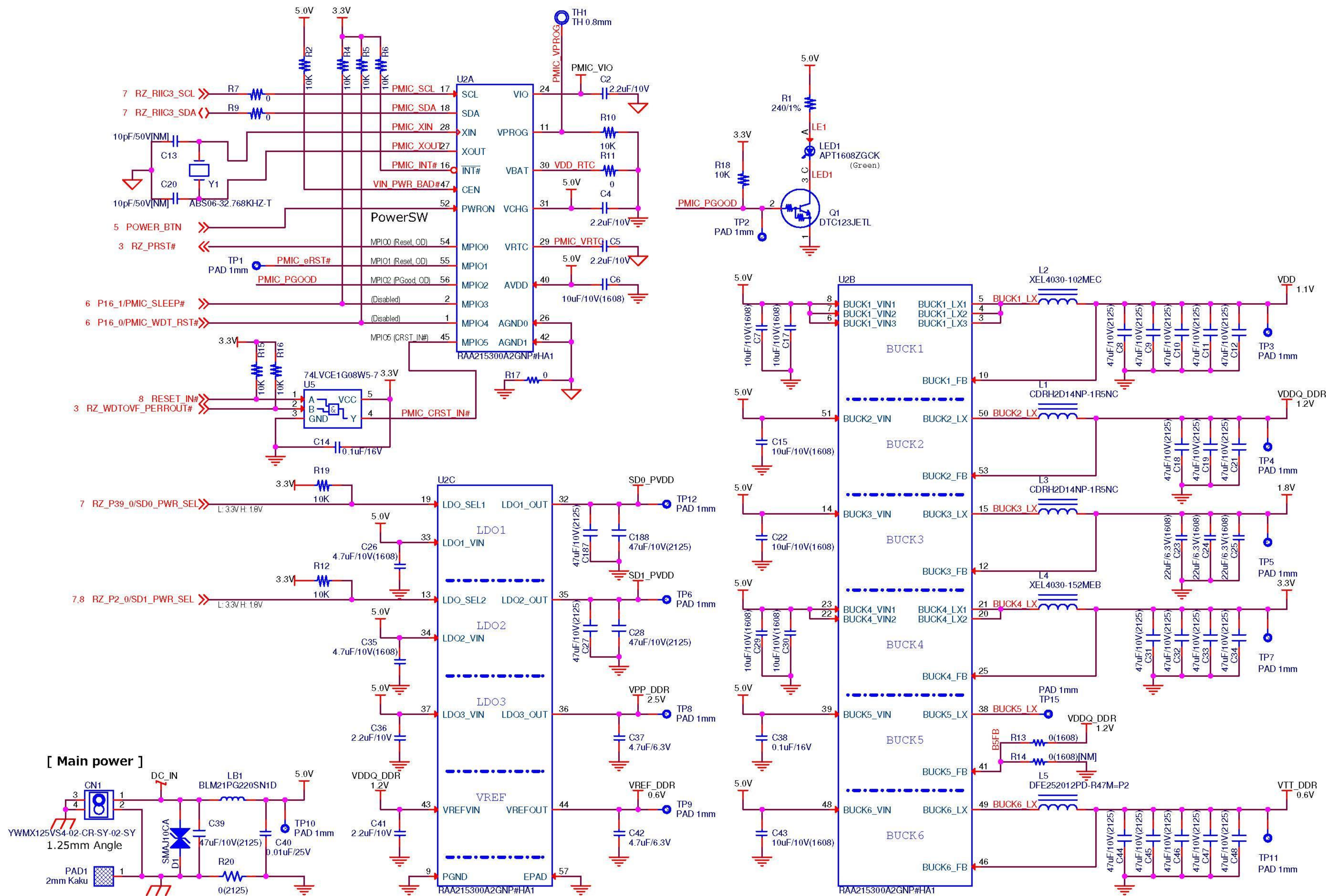
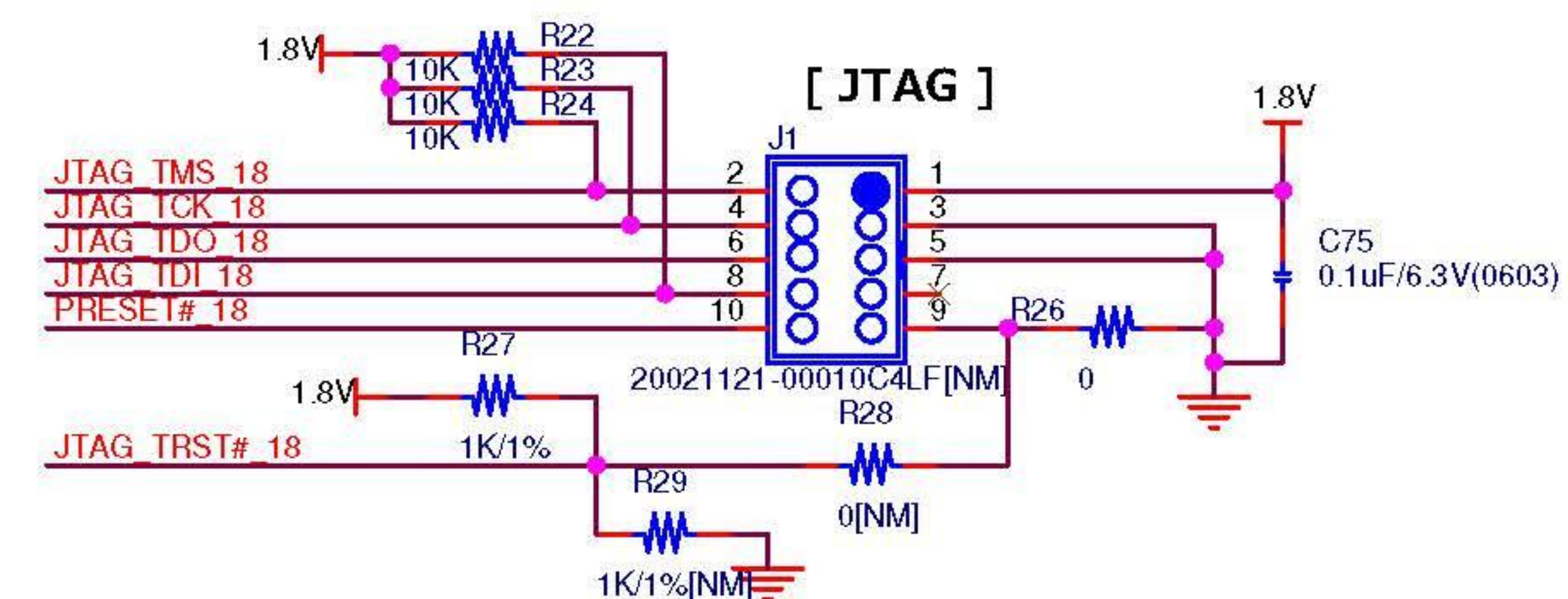
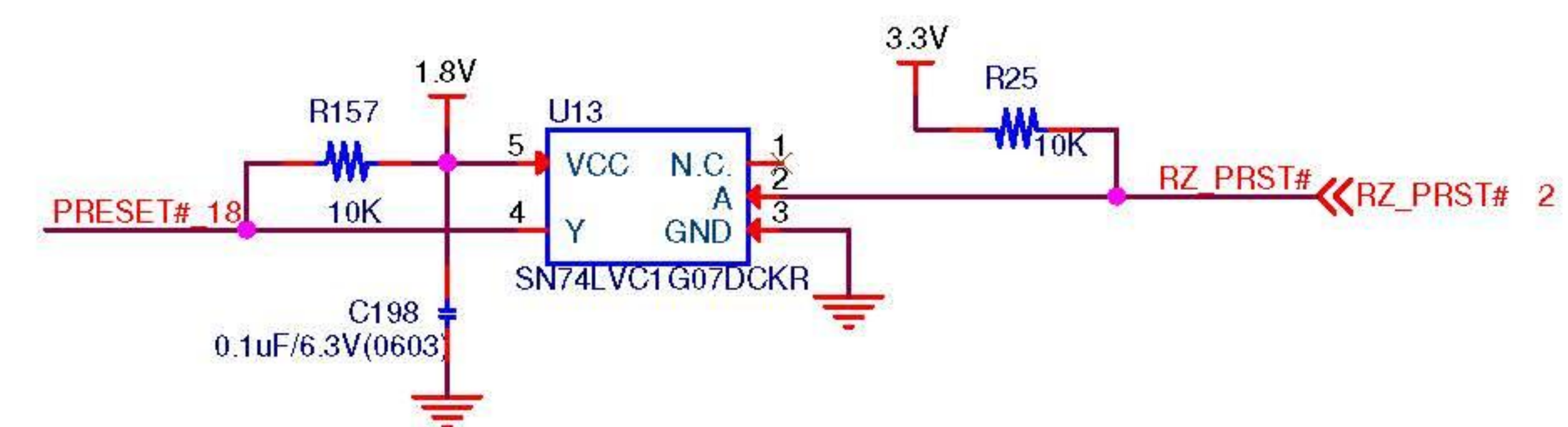
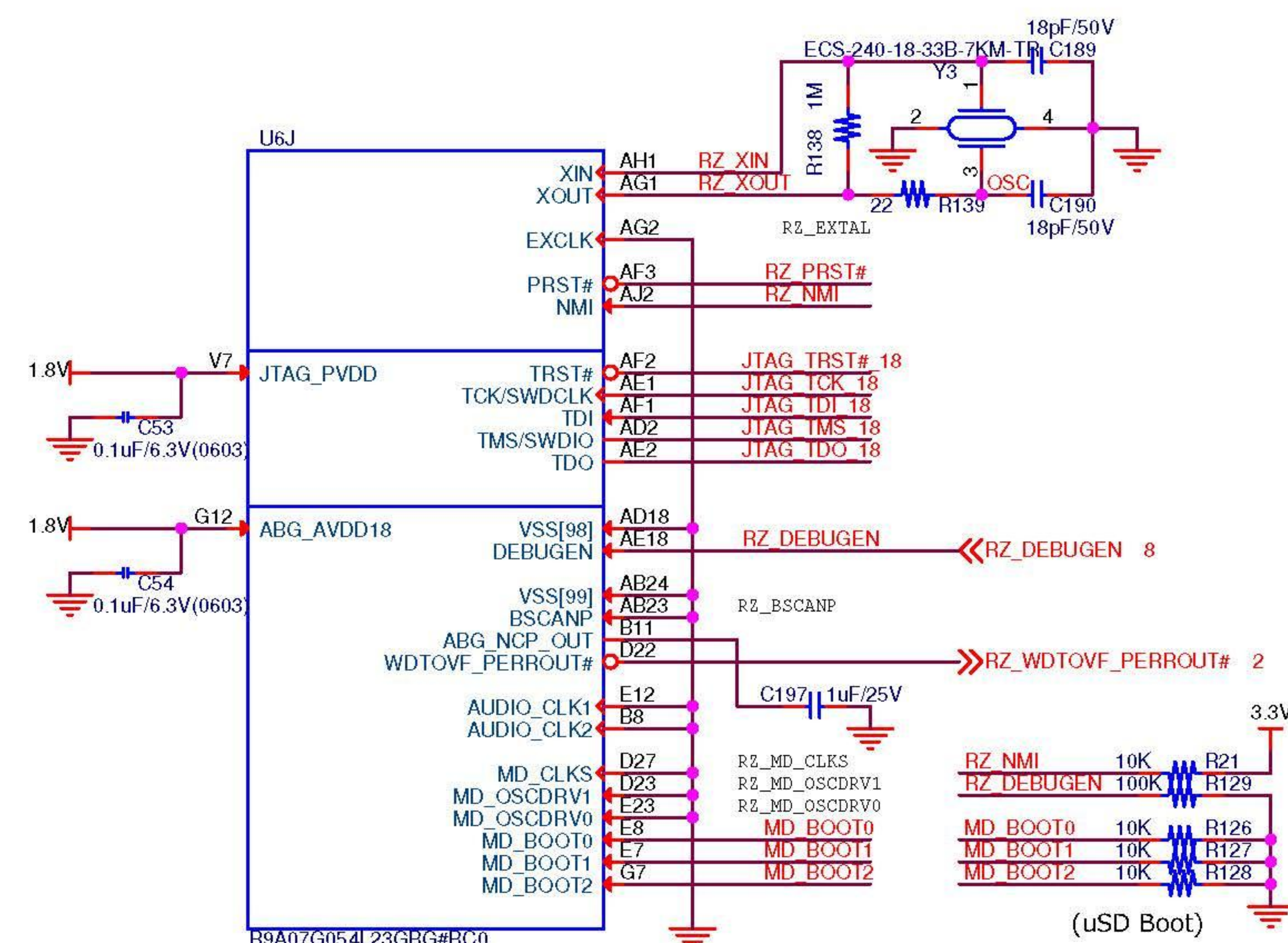
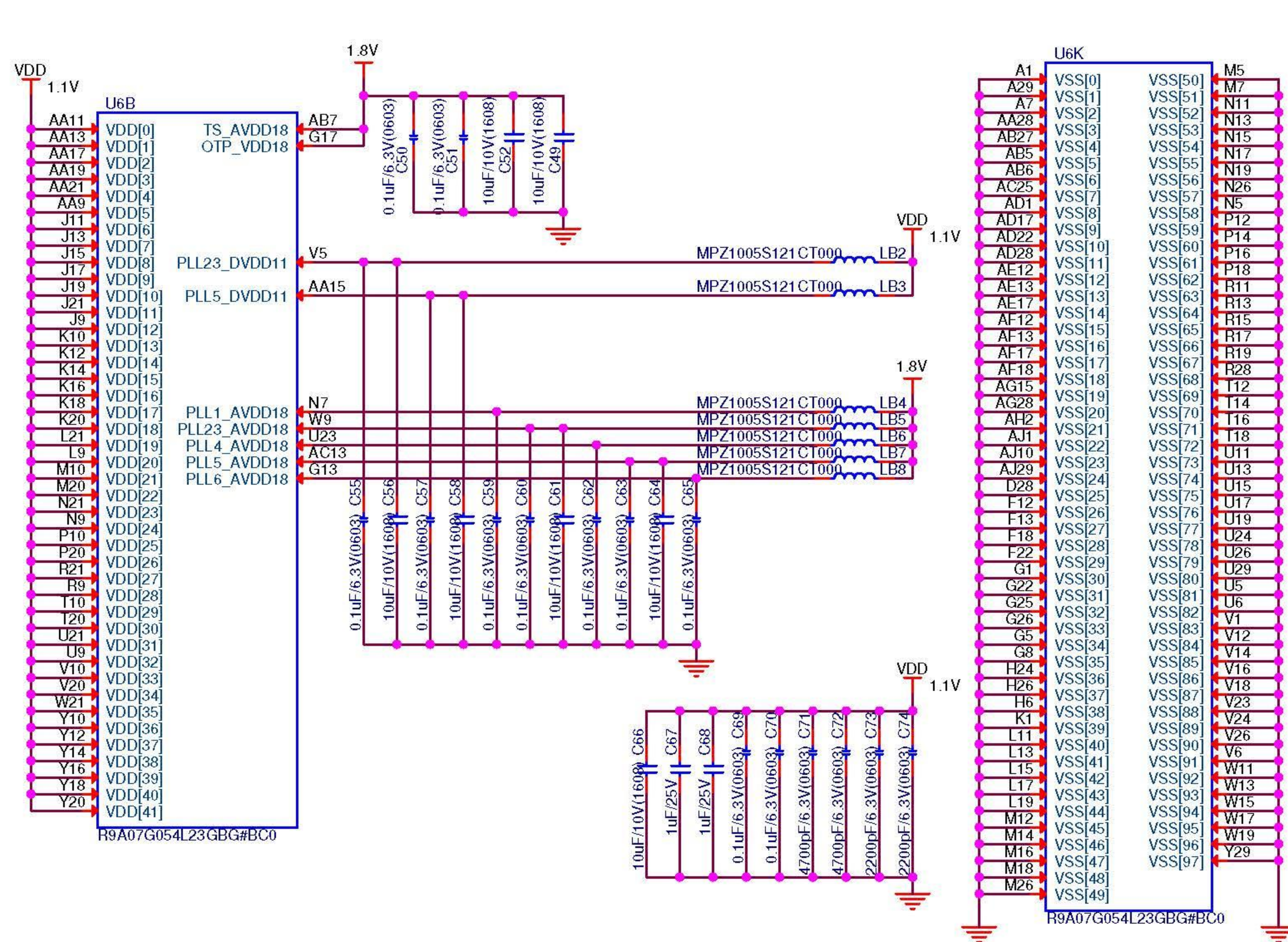


DC\_IN, PMIC(BUCK and LDO)





## RZ/V2L\_Power,Clock,JTAG

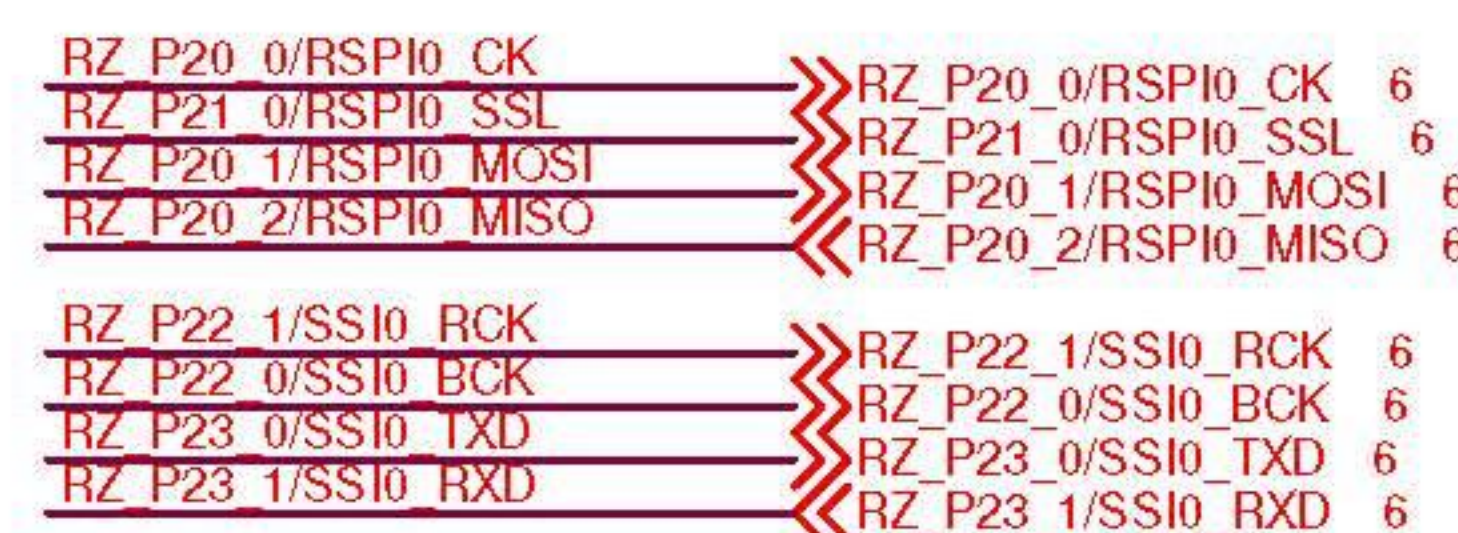
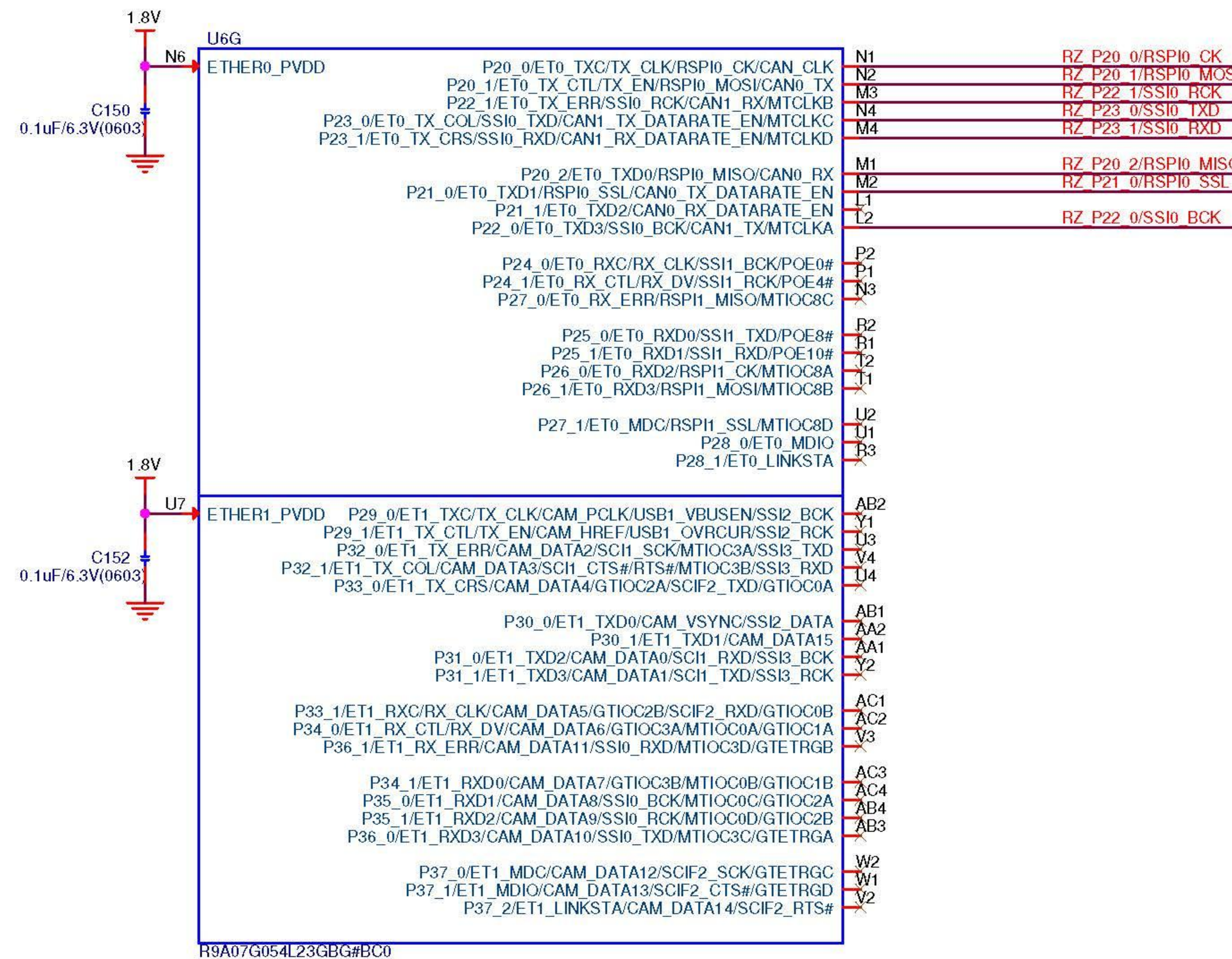
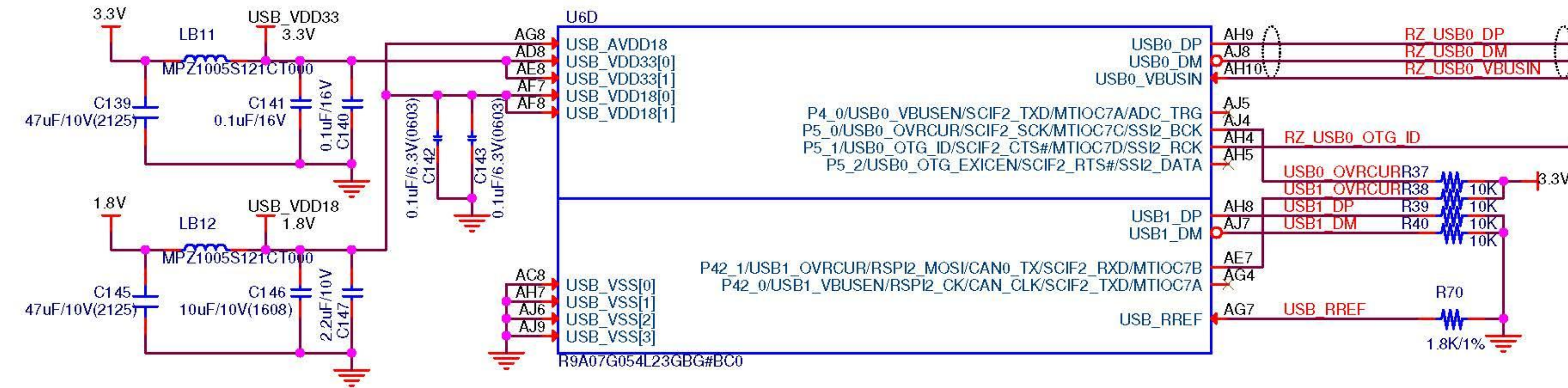








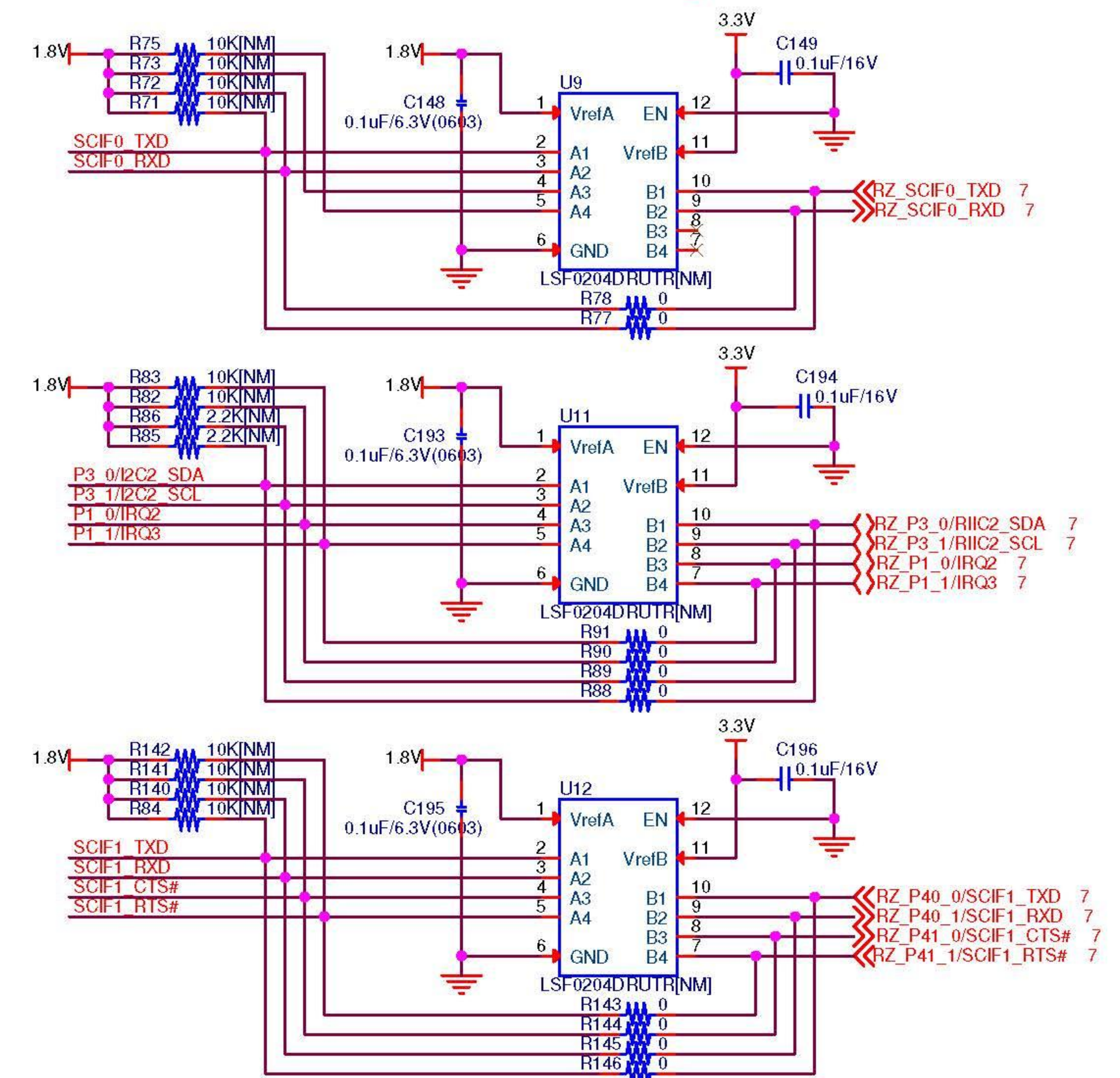
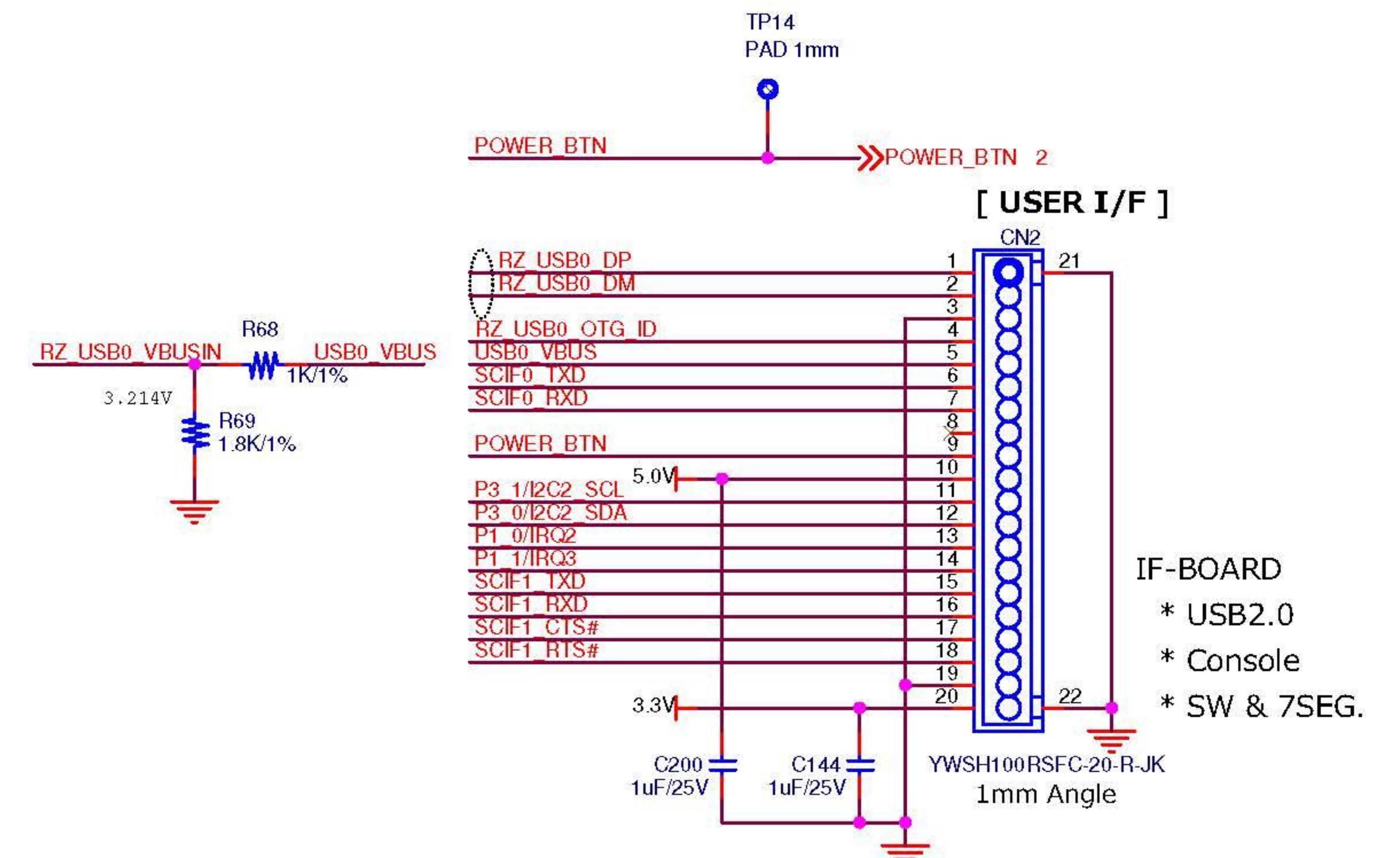
## RZ/V2L\_USB(User-Interface)



SENSOR BOARD

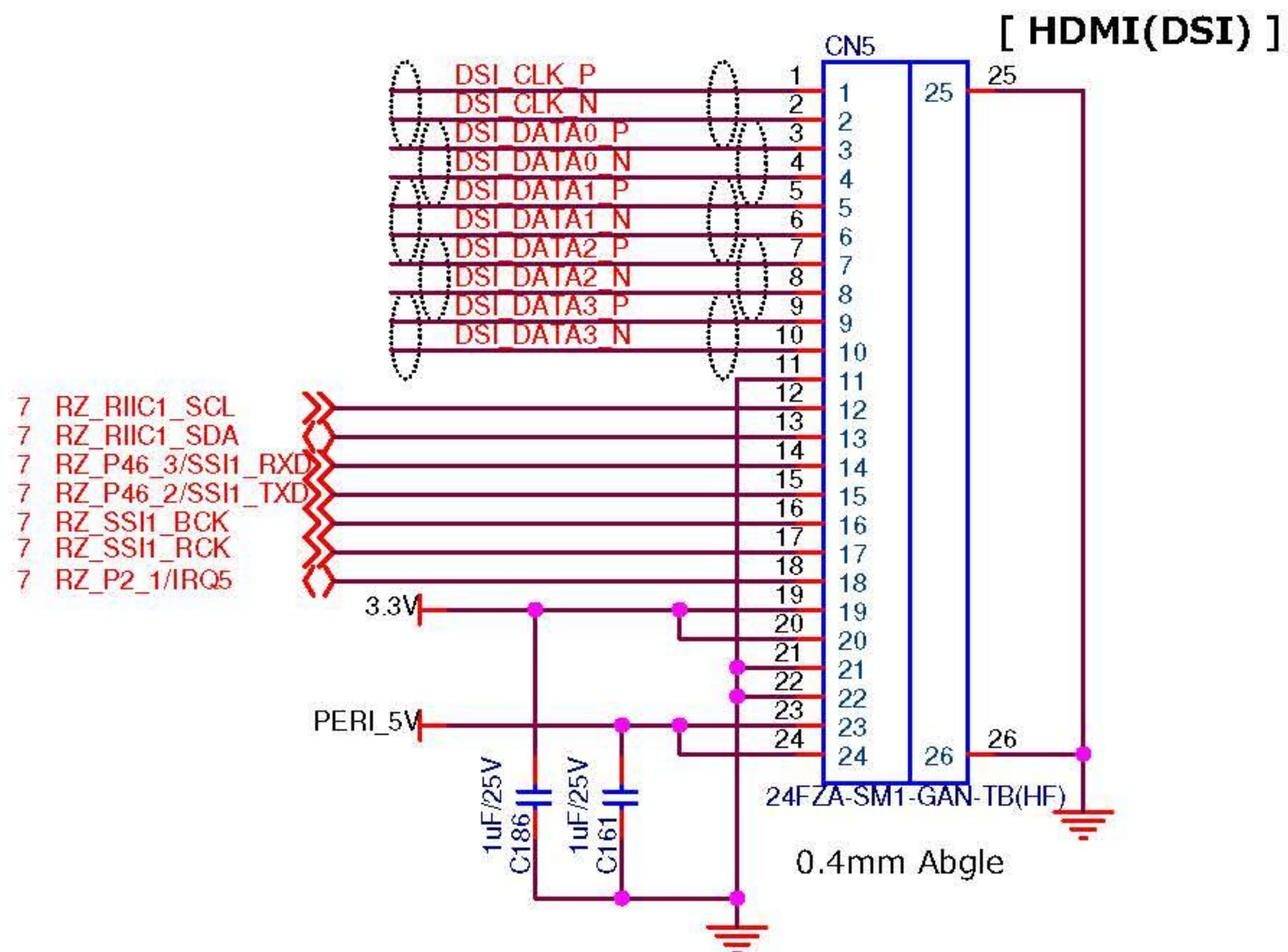
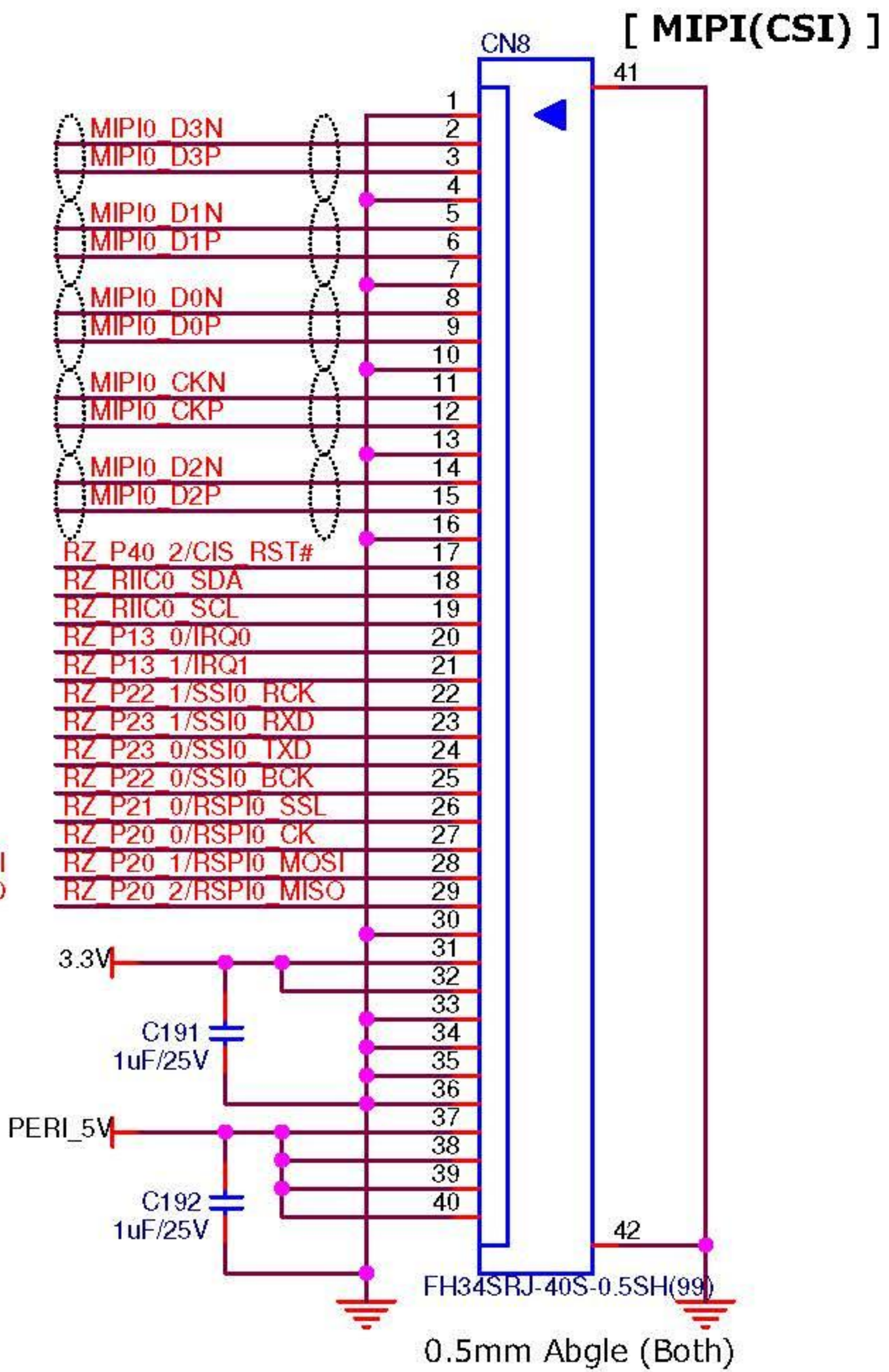
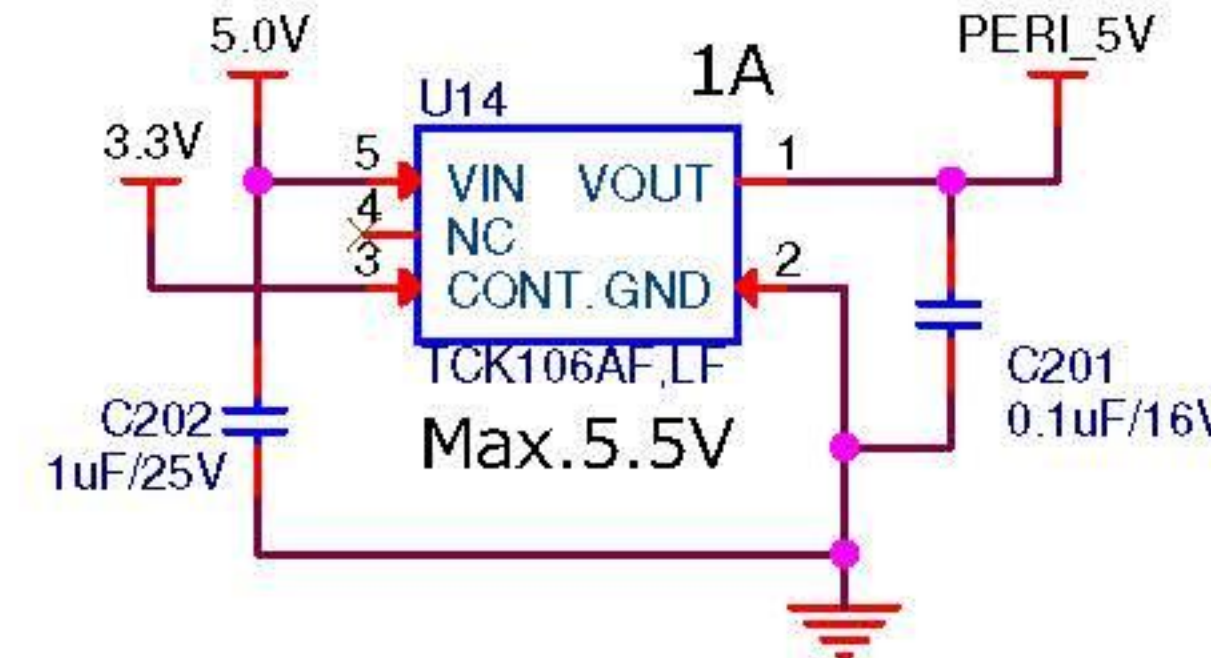
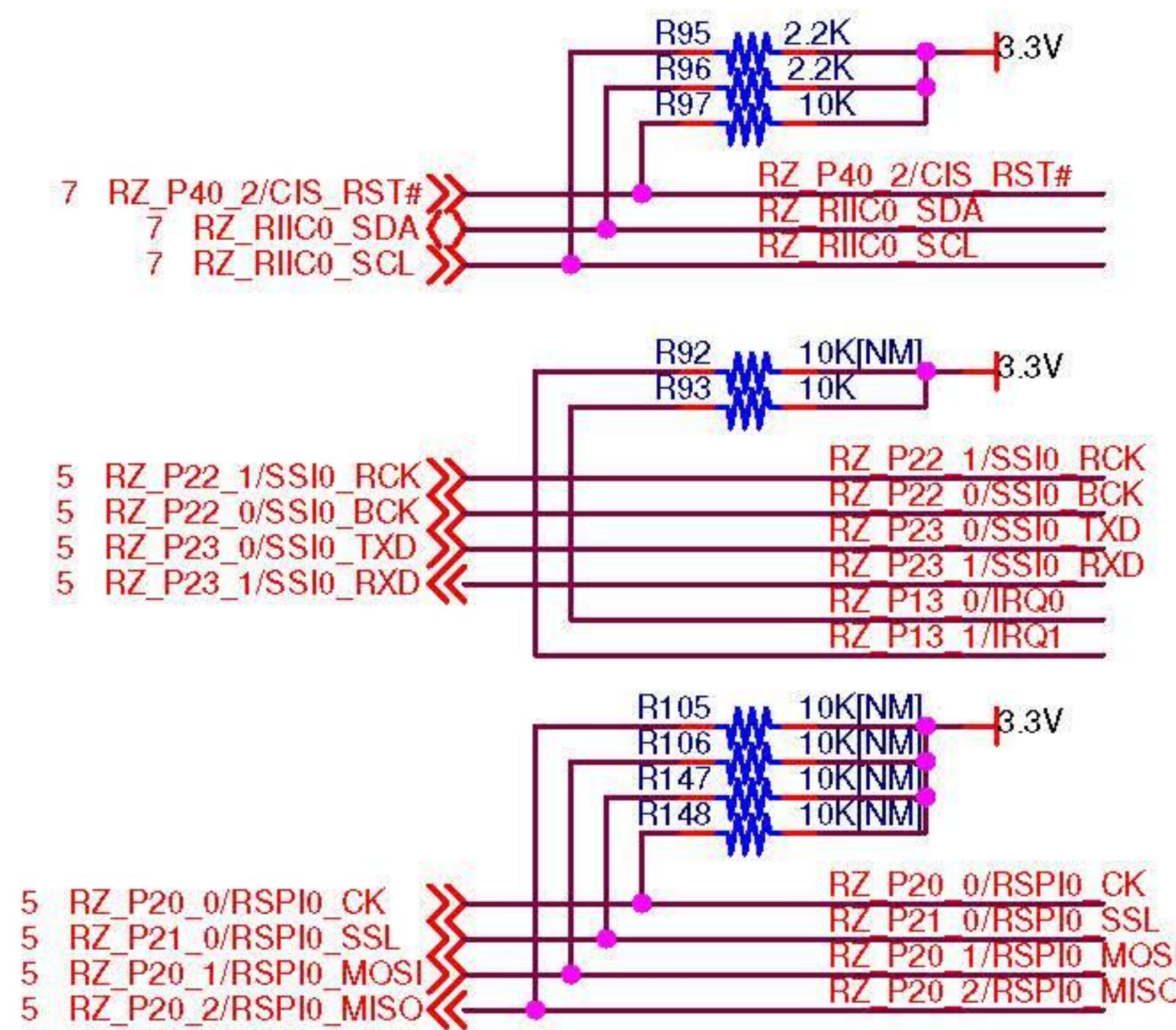
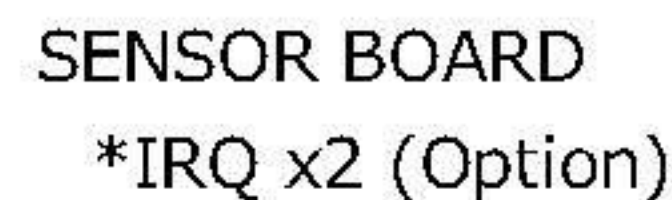
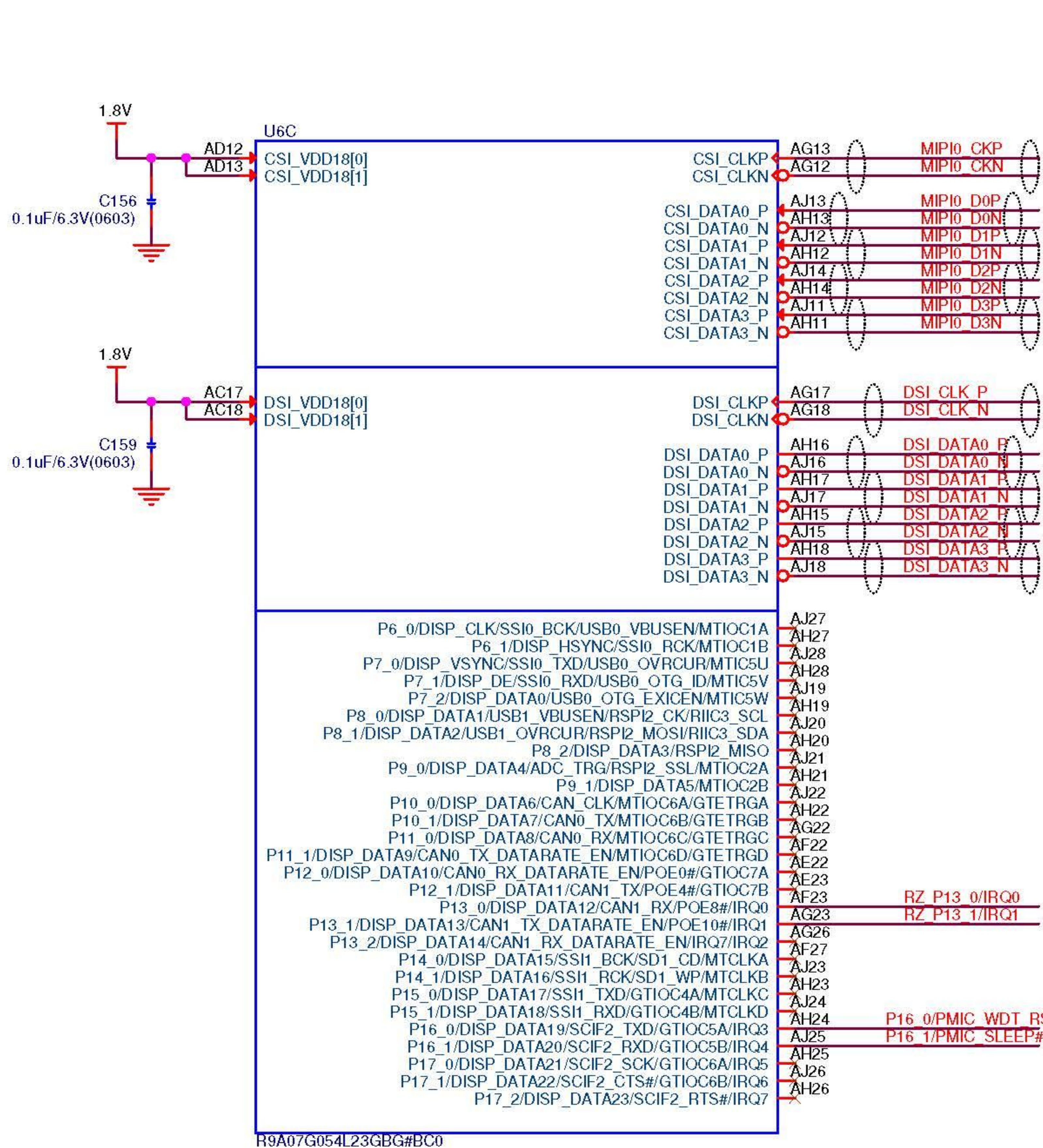
\*SPIC

\*GPIO x4



RZ/V2L_USB(User-Interface)			
Title			
RZ/V2L BOARD for EVK Rev.2			
Size	Document Number	Rev	
A3	SSCH_1877C_001_RZV2L_V3_2	1.0	
Date:	Wednesday March 13, 2024	Sheet	5 of 8







## RZ/V2L\_IO(Peripheral)

